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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/731,829	12/09/2003	Daniel J. Diessner	038190/267202	4409
826	7590 04/04/2005		EXAMINER	
	& BIRD LLP	NASRI, JAVAID H		
BANK OF AMERICA PLAZA 101 SOUTH TRYON STREET, SUITE 4000			ART UNIT	PAPER NUMBER
CHARLOT	TE, NC 28280-4000		2839	
			DATE MAILED: 04/04/2009	5

Please find below and/or attached an Office communication concerning this application or proceeding.

				H'Y		
		Application No.	Applicant(s)			
055 4-4 0		10/731,829	DIESSNER ET AL.			
	Office Action Summary	Examiner	Art Unit			
		Javaid Nasri	2839			
Period fo	The MAILING DATE of this communicate or Reply	ion appears on the cover sheet with	h the correspondence addres	is		
THE - External control	IORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICA ensions of time may be available under the provisions of 37 r SIX (6) MONTHS from the mailing date of this communicate period for reply specified above is less than thirty (30) dato period for reply is specified above, the maximum statutor ure to reply within the set or extended period for reply will, if reply received by the Office later than three months after the property of the	TION.  **CFR 1.136(a). In no event, however, may a repation.  ys, a reply within the statutory minimum of thirty y period will apply and will expire SIX (6) MONT by statute, cause the application to become ABA	ply be timely filed  (30) days will be considered timely.  HS from the mailing date of this commu	ınication.		
Status						
1)[🛛	Responsive to communication(s) filed o	n 18 February 2005				
		☐ This action is non-final.				
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
5)□ 6)⊠ 7)□	Claim(s) <u>1-43</u> is/are pending in the application of the above claim(s) <u>4,6-11,14-16,2</u> Claim(s) is/are allowed. Claim(s) <u>1-3,5,12,13,17-21,23,30,31,35</u> Claim(s) is/are objected to. Claim(s) are subject to restriction	<u>22,24-29,32-34,37,42 and 43</u> is/an , <u>36 and 38-41</u> is/are rejected.	e withdrawn from considerat	tion.		
Applicat	ion Papers					
10)⊠	The specification is objected to by the Ex The drawing(s) filed on <u>09 December 20</u> Applicant may not request that any objection Replacement drawing sheet(s) including the The oath or declaration is objected to by	<u>03</u> is/are: a)⊠ accepted or b)□ to the drawing(s) be held in abeyand correction is required if the drawing(s	ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.	.121(d).		
Priority (	under 35 U.S.C. § 119					
a)	Acknowledgment is made of a claim for the All b) Some * c) None of:  1. Certified copies of the priority documents.  2. Certified copies of the priority documents.  3. Copies of the certified copies of the application from the International See the attached detailed Office action for	numents have been received. Suments have been received in Ap ne priority documents have been r Bureau (PCT Rule 17.2(a)).	oplication No received in this National Stag	ge		
Attachmen	nt(s)					
2) 🔲 Notic 3) 🔯 Infor	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-9 mation Disclosure Statement(s) (PTO-1449 or PTO er No(s)/Mail Date <u>12/9/03</u> .	948) Paper No(s).	ımmary (PTO-413) /Mail Date ʻormal Patent Application (PTO-152 	2)		

## **DETAILED ACTION**

## Election/Restrictions

1. Applicant's election without traverse of specie V, figure 6, claims 1-3, 5, 12, 13, 17-21, 23, 30, 31, 35, 36, and 38-41 in the reply filed on 2/18/2005 is acknowledged. Claims 4, 6-11, 14-16, 22, 24-29, 32-34, 37, 42 and 43 are withdrawn from further consideration.

# Specification

- 2. The disclosure is objected to because of the following informalities:
  - a) On page 9, line 9, provide the missing information.

Appropriate correction is required.

# Claim Objections

- 3. Claims 17-34 are objected to because of the following informalities:
  - a) In claim 17, line 13, "third and fourth conductive elements" are already on line 11.
  - b) Check dependencies of claims 32 and 34.

Appropriate correction is required.

## Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

Art Unit: 2839

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5. Claims 1-3, 5, 12, 13, 17, 18, 21, 23, 30 and 31 are rejected under 35 U.S.C. 102(e) as being anticipated by Doriski, Jr. (6,554,639, cited in IDS)

The applied reference has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

Doriski, Jr. discloses, **for claim 1**, a plurality of component connection receptacles (A-Z); a plurality of first conductive elements extending from each component connection receptacle (see figure 3A); a plurality of second conductive elements (see figure 3A), wherein each second conductive element extends across at least one first conductive element (see figure 3B); and a plurality of connections between said first conductive elements and said second conductive

Art Unit: 2839

elements to provide interconnections, wherein said first and second conductive elements each comprise an insulative portion and a plurality of conductive portions (wires have insulation and conductors), for claim 2, the plurality of component connection receptacles comprise a plurality of connector shells and inserts (see figure 3A), for claim 3, each of said plurality of first conductive elements is connected at one end to an insert (see figure 3A), for claim 5, the plurality of connections between said first conductive elements and said second conductive elements comprise a plurality of connection vias between said first conductive elements and said second conductive elements (see figure 3A), a plurality of solder patches to connect two vias (see figure 3B) for claims 12, 21 and 30, the plurality of first and second conductive elements comprise a flat wire segment (see figure 3A), for claims 13 and 31, the plurality of first and second conductive elements comprise a printed circuit board (integrated modules are printed circuit boards), for claim 17, a plurality of component connection receptacles (A-Z), a plurality of first conductive elements extending from respective component connection receptacles (see figure 3A); a plurality of second conductive elements, wherein each second conductive element extends across at least one first conductive element (see figure 3B-D, 4A), and a plurality of connections between said first conductive elements and said second conductive elements to provide interconnections, a first backplane (ID21) comprising at least third and fourth conductive elements to provide interconnections; a second backplane (ID17) comprising at least third and fourth conductive elements to provide interconnections among the plurality of components associated with said first backplane; and a plurality of connection elements between said first and second backplane, wherein the conductive elements comprise an insulative portion and a plurality of conductive portions, for claim 18, the plurality of connection elements comprise

Art Unit: 2839

single wire, for claim 23, wherein at least one of the plurality of connections between said first conductive elements and said second conductive elements, the first backplane and the second backplane comprises a plurality of connection vias between at least one of the respective first and second conductive elements and third and fourth conductive elements and a plurality of solder patches, wherein each solder patch connects at least two of the connection vias (see figure 3D).

# Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claims 19, 20, 35, 36 and 38-41 are rejected under 35 U.S.C. 103(a) as being unpatentable over Doriski, Jr.

Doriski, Jr. discloses all the limitations of claim 17, as shown above,

However, Doriski, Jr. does not disclose:

a) The plurality of connection elements comprises coaxial cables/twisted pair wires. Official notice is taken that use of coaxial cables/twisted pair wires is well known in the art; therefore, it would have been obvious to one of ordinary skill in the art, at the time of the invention for Doriski, Jr to use coaxial cables/twisted pair wires to reduce the interference.

Application/Control Number: 10/731,829

Art Unit: 2839

b) For claims 35, 36 and 38, Doriski, Jr discloses all the structural limitations of claims as shown above, however, Doriski, Jr does not describe the method as claimed. The claimed method language is counter part of the apparatus claimed. Therefore, it would have been obvious to one ordinary skill in the art at the time the invention was made to develop a particular method for the disclosed apparatus of the cited references.

Page 6

#### Contact

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Javaid Nasri whose telephone number is 571 272 2095. The examiner can normally be reached on Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tulsidas C. Patel can be reached on 571 272 2800 ext 39. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Primary Examiner

Art Unit 2839

Application/Control Number: 10/731,829

Page 7

Art Unit: 2839

Jhn March 31, 2005